

behaviour [5]. However, there is one experimental evidence of natural 2D ferroelectric semimetal at room temperature: a few-layer WTe_2 [3] made by exfoliation, and another example is artificially made 2D polar metal based on superlattices $\text{BaTiO}_3/\text{SrTiO}_3/\text{LaTiO}_3$ using advantage of molecular beam epitaxy growth [6]. Both of these materials demonstrate a room-temperature ferroelectric effect. A ferroelectric structural transition occurs in bulk (3D) LiOsO_3 crystals at 140 K [7]. The interest in these materials has considerably grown due to the possibility of creating new quantum states, including coexisting ferroelectric, ferromagnetic, and superconducting phases [7], use them for functional nanoelectronics applications [2]. It is relevant to the combination of memory effects and conduction modulation, which improves transistor performance [8, 9].

Monolayer graphene (MLG) has high-mobility charge carriers at room temperature with acoustic phonon scattering as a limiting factor [10]. Bilayer graphene (BLG) demonstrates lower mobility at room temperature than MLG, but it is still much larger than any other semiconductors or semimetals known [11–13]. Therefore, it could improve the transport properties of ferroelectric metals based on vdW graphene heterostructures. Previously a breakthrough idea of changing properties of two-dimensional materials by staking them in a different order is useful for constructing the new artificially made vdW heterostructures [1]. For a ferroelectric metal to exist, several criteria need to be suggested: second-order phase transition, breaking inversion symmetry, polarization switchability [5].

Here we explore this general idea to create a recently discovered class of materials that combine semimetallic and ferroelectric properties in a single structure [14]. The effect of the moiré pattern reveals the possibility of forming a strong ferroelectric characteristic for conventional 2D materials such as hexagonal boron nitride (hBN) and BLG [15]. Breaking the inversion symmetry

occurs by mechanical stacking of two individual layers under small angle rotation, which opens a new path to create different physical effects in graphene, besides the well-known proximity of graphene to ferromagnets superconductors, 2D materials with spin–orbit interactions. Here we study electronic properties and reproduce the ferroelectric effect in bilayer graphene intercalated with a monolayer hBN up to 325 K and demonstrate a metal-to-insulator transition ferroelectric effect in graphene. We suggest a qualitative model for ferroelectricity based on the stress involved in the structure formed during fabrication.

2 Fabrication

Our device is a multiterminal Hall bar [Fig. 1(a)] made of quasi-twisted bilayer graphene (qTBG) using standard dry transfer technique [16]. Graphene layers in such qTBG are separated by a monolayer of hBN, allowing the graphene layers to be electron transparent. The sandwich is encapsulated between two relatively thick hBN slabs protecting MLG layers from the environment [Fig. 1(b)]. The qTBG heterostructure is connected to metal contacts [10] and endowed with top and bottom gate electrodes allowing independent control over the carrier density in each layer (n_t and n_b , respectively) and relative displacement field between the layers.

3 Results

Using the standard low-frequency lock-in technique, electrical transport measurements are carried out in a ^3He variable temperature inset system. Figure 1(d) shows our qTBG sample's resistivity as a function of back-gate voltage, V_{bg} , measured at $T = 2.1$ K and zero top gate voltage, $V_{tg} = 0$ V. It exhibits familiar for high-mobility BLG behaviour with a sharp peak of about

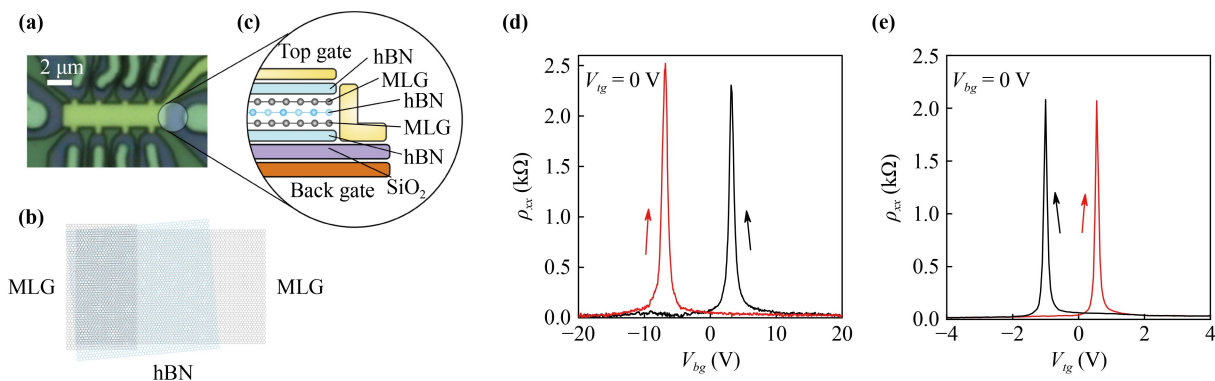


Fig. 1 Hexagonal boron nitride-separated quasi-twisted bilayer graphene. **(a)** Optical photograph of our Hall bar device. An encapsulated qTBG heterostructure is connected to metal leads (dull green) and endowed with gold top gate (bright green) and bottom silicon gate electrodes. **(b)** Schematic of the triple-layer structure. Two MLG layers are twisted by a small twist angle. **(c)** The schematic of the qTBG device with top and bottom gates. **(d, e)** The device's resistivity measured as a function of V_{bg} and V_{tg} at 2.1 K for $V_{tg} = 0$ V and $V_{bg} = 0$ V, respectively.

2 k Ω corresponding to the charge neutrality point (CNP), followed by a rapid decrease with increasing V_{bg} . When the gate voltage's sweep direction is reversed, the resistivity curve is shifted so that the CNP appears at 7 V, more than 10 V away from its initial position. The observed hysteresis is robust and reproduces itself for numerous gate (top and bottom) voltage sweep loops without an apparent sign of degradation of the hysteretic behaviour at low temperature, as shown in supplementary for the case of the resistivity colour map. A similar hysteresis is observed for the top gate sweep measured at $V_{bg} = 0$ V. Notice the CNP position, which is shifted to positive top gate voltages for the forward sweep and negative voltage for the backward sweep.

To confirm the existence of the hysteretic behaviour related to ferroelectricity, we demonstrate the resistance measurements at elevated maximum gate voltage (Fig. 2). Figure 2(a) shows resistance dependence on the back gate voltage measured at 2.1 K for different maximum voltages. At the start, the resistance maximum is observed around zero gate voltage, and the backward and forward sweeps have a small hysteresis. By increasing the maximum gate voltage range, the position of the resistance maximum shifts towards the polarity of the back voltage used without a limit. This effect potentially has two explanations: the presence of the ferroelectric state or the charged traps with slow dynamics. To rule out the trap mechanism, we have measured the presence of spontaneous polarisation, which is clearly seen in Fig. 2(b) for the top gate voltage backwards and forward sweeps. Starting from a small voltage range, the resistance does not cross the resistance maximum, but with increasing the gate voltage range, the resistance peak appears, and it does not change its position significantly for the same polarity of the top gate. It is an indication of the maximum spontaneous polarisation in our structure. The transients characteristics on Fig. 2(c) are revealing a bistable state that is characteristic for regular ferroelectric polarization and demonstration of the possible usage of the device for a transistor with memory. More data on the memory effect are shown in the supplementary.

We review the observed hysteretic behaviour of resistivity of the qTBG intercalated with monolayer hBN in more detail. Previously, a moiré heterostructure hBN/bilayer graphene/hBN has demonstrated a similar hysteresis at low temperatures [15]. The resistivity map is measured as a function of the back gate voltage which charges from -40 V to 40 V for the forward sweep Fig. 3(a) and 40 V to -40 V for the backward sweep [Fig. 3(b)]. Both maps are measured at 2.1 K, and the top gate voltage is fixed during the sweep. The top gate voltage changes from negative to positive values (from -5 V to 4 V). The dark region shows a noticeable difference in the resistivity peak position between the forward and backward sweeps. We plot Fig. 3(c) the maximum resistivity for the forward and backward back gate voltage sweeps to characterize the hysteresis. The hysteretic behaviour illustrates internal polarization in the heterostructure [15], which changes the charge carrier concentration in both graphene layers. The external electric field created by the back and top gate voltages can reverse this polarization by applying a large back gate voltage ($|V_{bg}| > 10$ V) or top gate voltage $V_{tg} < -2.2$ V and $V_{tg} > 1.5$ V. The peak shifts linearly in the range of V_{bg} from -9.1 V to 9.7 V, which corresponds to a linear polarization for ferroelectrics [17]. The top gate's efficiency with respect to the bottom gate cannot be determined from the top and bottom hBN thicknesses (42.6 nm and 111 nm, respectively) using a small dielectric constant for hBN. If the relative dielectric constant for hBN is taken as 5, then the expected efficiency is about 11.6. However, the dashed lines in Fig. 3(c) correspond to the ratio of $V_{bg}/V_{tg} = 7.6 \pm 0.1$. Therefore, the dielectric environment of graphene is significantly distorted by the ferroelectric effect. In the regions without hysteresis, the resistivity peak shift also does not correspond to the gate's efficiency defined as a coefficient of proportionality, α , between the concentration and the gate voltage ($n = \alpha V_g$). The device undergoes ferroelectric-paraelectric phase transition after thermal cycling as shown in Figs. 3(a)–(c) (see supplementary for the details). In the paraelectric phase, the dielectric constant is linear, and the resistivity of the structure depends strongly on the

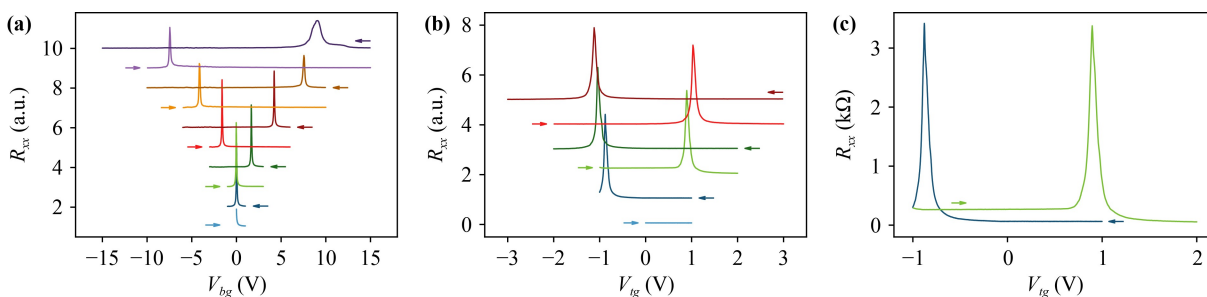


Fig. 2 Gate voltage sweeps with increasing maximum voltage. (a) Resistance as a function of V_{bg} for different maximum back gate voltage. The curves are shifted up for clarity. (b) Resistance as a function of V_{tg} for different maximum top gate voltage. The curves are shifted up for clarity. (c) Two scans from the image (b) shows the memory effect.

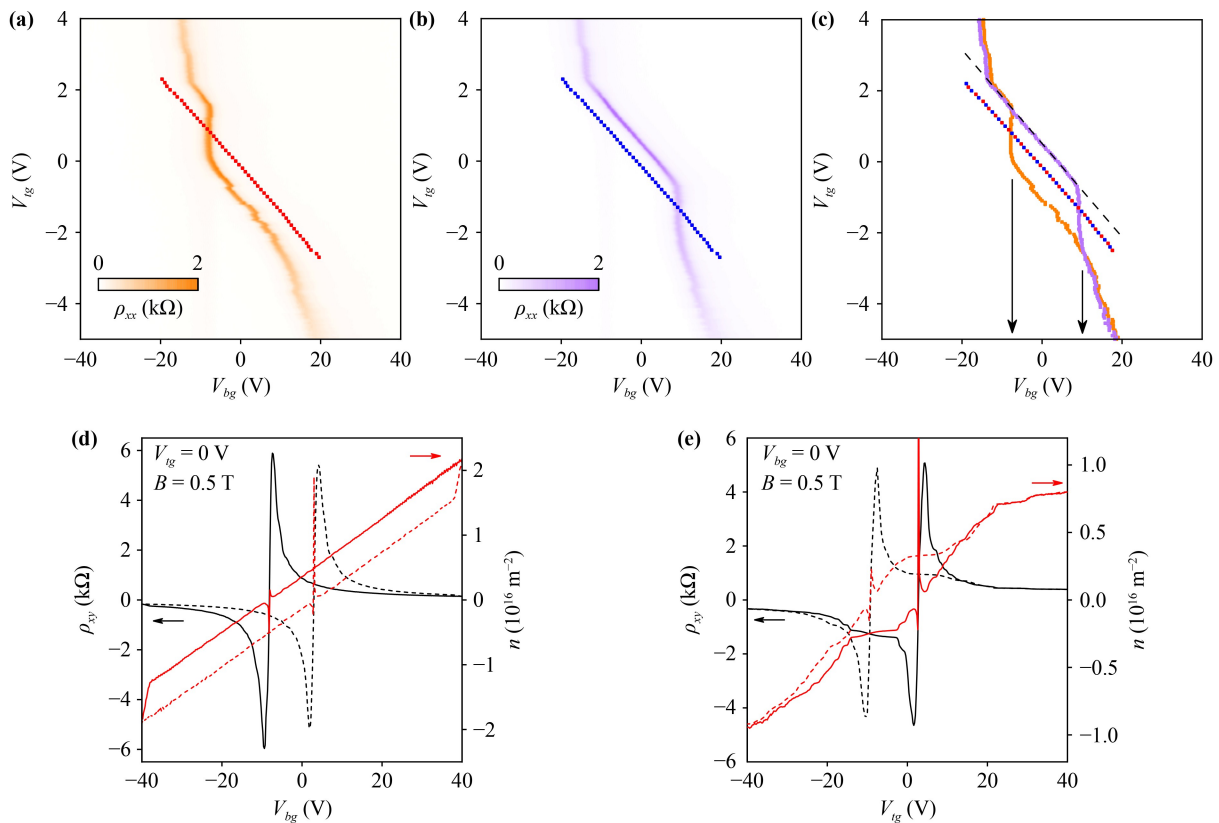


Fig. 3 Ferroelectric hysteresis at 2.1 K. **(a, b)** The sample's resistivity as a function of the back gate and top gate voltages for the forward (a) and backward (b) back gate voltage sweeps at fixed V_{tg} . Scattered data show the position of the maximum resistivity for a paraelectric phase. **(c)** The maximum of the resistivity for the forward and backward as a function of the top gate and back gate voltages. The dashed line is the best fit for the forward and backward sweep's resistivity maxima in the linear ferroelectric regime. The blue and red squares show the maximum resistivity for a paraelectric phase. **(d)** Hall effect measured at $B = 0.5$ T for forward (solid black curve) and backward (dashed black curve) sweeps as a function of back gate voltage at $V_{tg} = 0$ V. The red curves show corresponding concentrations calculated from the Hall voltage. **(e)** Hall effect measured at $B = 0.5$ T for the forward (solid black curve) and backward (dashed black curve) sweeps as a function of top gate voltage at $V_{bg} = 0$ V. The red curves show corresponding concentrations calculated from the Hall voltage.

properties of hBN layers. To the best of our knowledge, this observation has not been reported before in 2D heterostructures.

The average concentration in the system then can be determined by the Hall effect (see supplementary). Calculations of the Hall concentration as a function of back-gate voltage at $V_{tg} = 0$ V are shown in Figs. 3(d) and (e). The resistivity maximum is shifted between the forward and backward shift on the same voltage when the efficiency of the top gate is taken into account. We notice here that the view of this hysteresis and the positions of maxima and minima do not change if top gate voltage sweeps are used at fixed V_{bg} (See supplementary).

Finally, we characterized the temperature dependence of the sample's resistivity for the forward and backward sweeps. The resistivity maximum is reducing with increasing temperature as expected for both MLG and BLG, and the separation between peaks, which is related to spontaneous polarization in our structure, is decreasing with increasing temperature. Still, it does not disappear entirely, even at the maximum available

temperature of 325 K [Fig. 4(b)]. The full data set is shown in the supplementary. The cross-over density between metallic ($d\rho/dT < 0$) and insulator ($d\rho/dT > 0$) states is equal to $5 \times 10^{10} \text{ cm}^{-2}$, which is in agreement with previous report [18]. The corresponding mobility is found to be better than $10 \text{ m}^2 \cdot \text{V}^{-1} \cdot \text{s}^{-1}$ at room temperature [Fig. 4(d)], which is higher than in bilayer graphene [19] and comparable with acoustic phonon limited mobility in monolayer graphene [10]. The charge carrier mobility reduces with increasing temperature, as shown in Fig. 4(d) linearly.

4 Discussion

Previously a strong ferroelectric effect was observed in aligned and rotated by 30° hBN/bilayer graphene heterostructures [15]. The authors argued that the different parts of a supercell could induce spontaneous polarization in such a structure. Here we study a similar system without intentional alignment between hBN and

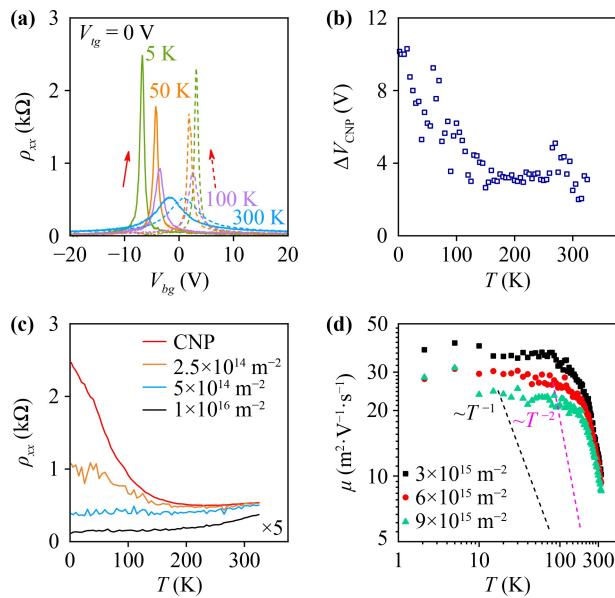


Fig. 4 Transport properties at high temperatures. **(a)** The temperature dependence of the resistivity as a function of back gate voltage at $V_{fg} = 0$ V for the forward (solid style) and backward sweeps (dashed style) for four selected temperatures. **(b)** The voltage difference between CNP positions of the backward and forward sweeps. The temperature changes from 2 K to 325 K. **(c)** The temperature dependence of the resistivity for the forward sweeps for different electron concentrations. The smallest resistivity is multiplied by 5. **(d)** The mobility of electron gas as a function of temperature measured at $3 \times 10^{15} \text{ m}^{-2}$, $6 \times 10^{15} \text{ m}^{-2}$ and $9 \times 10^{15} \text{ m}^{-2}$. The dashed lines are guides for the eye.

graphene layers. The angle between the top and bottom MLG is less than 2° . The orientation of the hBN edges and MLG are random. The monolayer hBN edges are not visible in the final stack. The absence of any supercell at low energy could be justified by the gate voltage dependence of the resistance. In this case, the band structure should have more features at low energy, which creates extra CNPs as was reported in Ref. [20]. Also, our device does not show Brown–Zak oscillations [21], which could be an indication of alignment between graphene and hBN. Therefore, our system’s ferroelectric effect could not be of the same origin as in the intentionally aligned graphene/hBN structure [15]. However, we could not convincingly exclude 30° rotation [15] or the inversion symmetry breaking in the vertical graphene/hBN/graphene heterostructure as a cause of ferroelectricity. The twisted hBN layers can demonstrate the ferroelectric effect [22]. This observation was reported in Refs. [23, 24]. We can assume that the monolayer hBN can be easily twisted by sliding between two graphene layers. In that case, the polarization can be created by the top hBN and monolayer hBN or between monolayer hBN and bottom hBN layers for the case of 30° rotation [15].

Now we can speculate on the possible origin of the ferroelectric effect. We notice that the structure is made

at the 110°C , which is a standard temperature for the dry transfer procedure. It is known that the thermal coefficient of expansion for hBN is negative [25]. Therefore, a fast cooldown of the heterostructure made of the monolayer of BN placed between the thick BN layers on SiO_2 substrate (with a positive thermal coefficient of expansion) induces the film’s strain. At low temperatures, it expands. Previously, piezoelectric strains have been measured in BN bubbles of monolayer BN [26]. The effect is related to the polar lattice of BN. To form an out-of-plane dipole, the B-sublattice should be shifted up or down with respect to the N-sublattice. This effect will partially negate in-plane strain in the film. The polarization of a single layer of BN should be proportional to the square root of temperature due to geometrical consideration near room temperature as the thermal expansion is a linear function of temperature near room temperature [25]. Asymmetry between the top and bottom gate is possibly caused by different Si and top gate metal thermal expansion. If some N atoms are aligned with B atoms, then a region with no polarisation is formed. This effect reduces the overall polarisation of the structure but increases the strain, which needs further investigation.

Another explanation often used to explain hysteretic behaviour is related to charge traps in the dielectric layer. It is common to deep observer levels in SiO_2 due to defected structure. However, the distance from the conducting layer is too long, and therefore the charging state of these levels does not change with the externally applied voltage. It could be viewed as a small fixed voltage in the capacitance formed by the charged layer and the conducting plate. Therefore the hysteresis could be observed only with respect to thermal cycling when the charging of these traps can be changed. The smaller distance between the traps and the MLG is an essential requirement for the resistance hysteresis as a function of gate voltage. The symmetry between positions of the maxima in forwarding and backward directions could be caused by the symmetric energy distribution of the charged traps close to graphene layers. Potentially the presence of the state without hysteresis after thermal cycling [for example, Fig. 3(a)] could be explained by the strong energy shift of these traps after warming up. That distribution is unlikely as it is not observed in any systems except to a single type of impurity or defect. In that case, the impurity states should be located near the Dirac point in graphene; otherwise, it contradicts the symmetric distribution of the resistance peak position. Due to the strong dependence of the tunnelling on the distance, the space distribution is also delta-like; otherwise, the resistance peak should be broad as it will depend on the exponentially long tunnelling times. Therefore the required distribution of the levels to explain hysteretic behaviour should fulfil two strict criteria: zero energy with respect to the graphene Dirac point and the same position with respect to the

graphene layers. However, the tunnelling probability depends exponentially on the temperature, and the time position of the neutrality points between the forward and backward sweeps should also demonstrate exponential behaviour, which is not observed experimentally (see supplementary). Nevertheless, the observation of the switch of the Hall voltage as shown in Fig. 3(e) could not be explained by any trap distribution. Also, the mobility of our structure is very high, and in the case of closely separated charged traps, the resistance peak should be broadened. We dismissed the trap origin of the observed hysteresis.

Mobility of the charge carriers in MLG at high temperature is limited by acoustic phonon scattering [10]. The temperature dependence of mobility is inversely proportional to temperature. This observation is valid for graphene and its bilayer [27]. The temperature dependence of resistivity is proportional to $\rho \propto T$ in the case of twisted bilayer graphene, which was observed experimentally [28] and predicted theoretically [29] for small angles of rotation. Our observations demonstrate inverse parabolic dependence of the mobility $\mu \propto T^{-2}$ as shown in Fig. 4(d), which does not contradict to the contribution of optical phonons in hBN substrate [30] or polar optical phonons [31]. However, the most probable origin is the effect of Coulomb drag resistance in the double graphene layer structures separated by a thick hBN film (> 1 nm) without interlayer tunnelling also demonstrates $\rho \propto T^2$ dependence [32]. The deviation of mobility from this dependence at low temperature is explained by scattering at the edges of the sample.

We have demonstrated ferroelectric–paraelectric transition in metal-like ferroelectric based hBN intercalated double-layer graphene. The high-mobility graphene ferroelectricity survives above room temperature, which shows promising for ferroelectric transistors.

5 Methods

5.1 Transport measurements

The device was measured in an Oxford Instruments TeslatronPT cryogen-free superconducting magnet system equipped with Oxford Instruments HelioxVT Sorption Pumped ^3He Refrigerator insert (300 mK, 14 T) and the magnetic field applied perpendicular to the plane of the film. Stanford Research Systems SR830 lock-in is used to apply an AC bias current with a 100 M Ω bias resistor at a frequency of 13.333 Hz, and Keithley 2614B SourceMeters were used to apply DC current with a 100 M Ω bias resistor. Keithley 2400 SourceMeters were used to apply voltages to the gates.

5.2 AFM and Raman measurements

AFM measurements are performed with a Bruker

Dimension Fastscan system at tapping mode. Scan area of the bottom/top hBN are shown in the right/left red dotted boxes in the supplementary. The length to width ratio is 1.16. The width of the sample is 2.7 μm . The top and bottom hBN thicknesses are equal to 42.6 nm and 111 nm, respectively.

Room temperature Raman scattering is performed using a WITec/alpha 300R confocal microscope with a 532 nm laser under ambient conditions. The laser power was kept below 1 mW to avoid damage or heating. The G and 2D peaks in the Raman spectra are fitted with Lorentzian. Typical Raman spectra of different positions of the heterostructure are plotted in the Supplementary.

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Author contributions statement A.S.M. and G.Y. designed the project. Y.W. fabricated the samples, S.J. and J.X. performed transport measurements, X.C. and G.M. did the AFM and Raman research, K. W. and T.T. provided hBN crystals. S. J., J.X., Y.W., and A.S.M performed data analysis, D.Z., P.W., G.M., Y.H, J.H., and A.S.M provided the experimental support. Y.W., A.S.M and G.Y. wrote the manuscript. All authors participated in the discussions.

Competing interests The authors declare no competing interests.

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