



Correction to: An ultra-compact polarization-insensitive slot-strip mode converter

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Correction to: Frontiers of Optoelectronics (2022) 15:5
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Following publication of the original article [1], the authors identified an error in Fig. 3b. The correct Fig. 3 is in this correction, and the original article has been corrected.

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Reference

1. Tao, Z., Wang, B., Bai, B., Chen, R., Shu, H., Zhang, X., Wang, X.: An ultra-compact polarization-insensitive slot-strip mode converter. *Front. Optoelectron.* **15**, 1–7 (2022). <https://doi.org/10.1007/s12200-022-00008-5>

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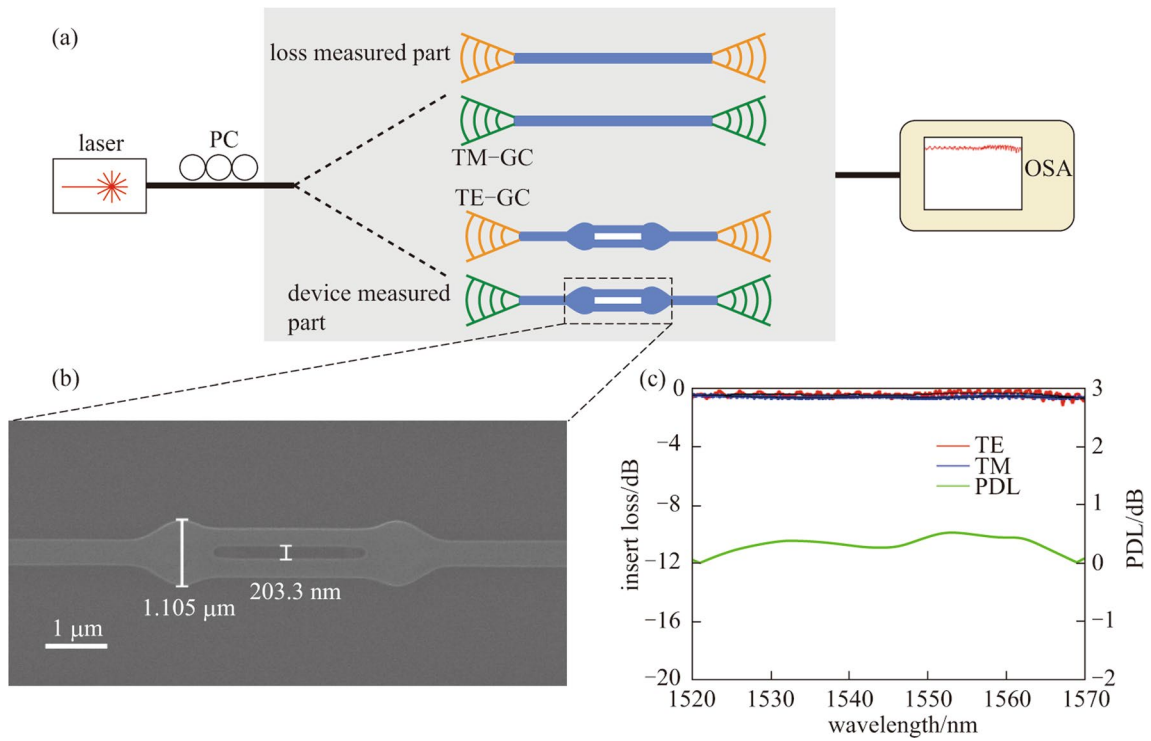


Fig. 3 **a** Schematic of the device measurement. PC, polarization control; TM-GC, TM polarization sensitive grating coupler; TE-GC, TE polarization sensitive grating coupler; OSA, optical spectrum analyzer. **b** Top-view SEM picture of fabricated converters. **c** Measured transmission of TE₀ mode (red line) and TM₀ mode (blue line); the black lines are trend lines by robust locally weight regression method. The green line represents the polarization dependent loss (PDL)