



Erratum:

Erratum to: Folded down-conversion mixer for a 60 GHz receiver architecture in 65-nm CMOS technology

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In the original article, Fig. 6 was incorrect. The correct version is given below.

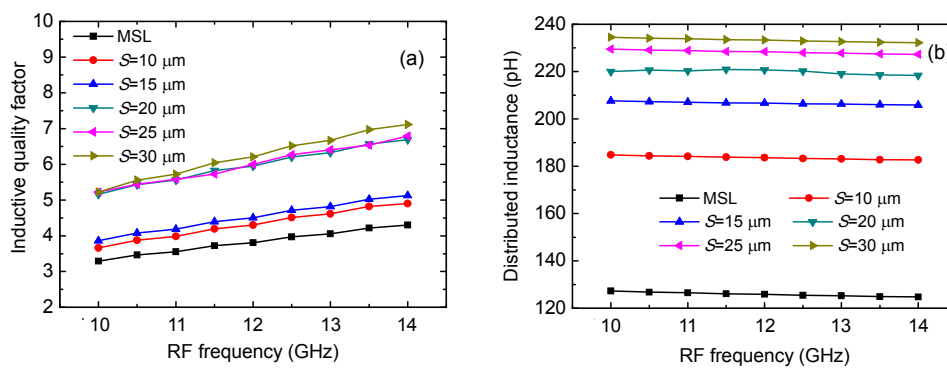


Fig. 6 Inductive quality factor vs. input frequency range with varying S (a) and distributed inductance vs. input frequency range with varying S (b)

The online version of the original article can be found at <http://dx.doi.org/10.1631/jzus.C1400087>

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